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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/635,801	08/05/2003	Kotaro Terada	393032039800	8825
7590 12/12/2007 David L. Fehrman Morrison & Foerster LLP 35th Floor 555 W. 5th Street Los Angeles, CA 90013			EXAMINER SUTHERS, DOUGLAS JOHN	
			ART UNIT 2615	PAPER NUMBER
			MAIL DATE 12/12/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/635,801	TERADA ET AL.	
	Examiner	Art Unit	
	Douglas Suthers	2615	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 22 August 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-13 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-13 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 August 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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DETAILED ACTION

The Art Unit location of your application in the USPTO has changed. To aid in correlating any papers for this application, all further correspondence regarding this application should be directed to Art Unit 2615.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-12 are rejected under 35 U.S.C. 102(b) as being anticipated by Davis (US 5454041).

Regarding claim 1, Davis discloses a mixing signal-path setting apparatus for making a setting of a signal path in an audio mixing system, said audio mixing system comprising a plurality of input channels and a plurality of output channels, said audio mixing system performing signal mixing by connecting each of plural input channels, selected from the plurality of input channels to at least one output channel selected for the input channel from among the plurality of output channels, said mixing signal-path setting apparatus comprising:

a first signal path setting section (Figure 3, mix/assign pins 138) that selects plural input channels from among the plurality of input channels and makes a setting such that signals of the selected plural input channels are mixed into a first output

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channel (figure 2, to item 26) selected from among said plurality of output channels, so that signals obtained by mixing the signals of all the selected plural input channels, are outputted via said first output channel;

an exclusion setting section (direct/mix-minus select pins 110, and associated circuitry) that selects a particular input channel from among the plurality of input channels, to be excluded; and

a second signal path setting section (switch 200, node 288, 232 and surrounding circuitry) that makes a setting such that the signals of the plural input channels selected via said first signal path setting section, having the signal of the particular input channel selected by said exclusion setting section excluded therefrom, are mixed into a second output channel (to item 34) of the plurality of output channels, so that signals obtained by mixing the signals of the selected input channels with the particular input channel excluded therefrom, are outputted via said second output channel.

Regarding claim 2, Davis discloses an apparatus that further comprises an output channel setting section (interface of 10 in figure 2) that sets, as said second output channel, a desired one of the plurality of output channels other than said first output channel (two different output channels).

Regarding claim 3, Davis discloses wherein said exclusion setting section (138) selects the particular input channel to be excluded from the plurality of input channels and cuts the signal (node 288) of the selected particular input channel so as to prevent

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the signal of the selected particular input channel from being led to said second output channel.

Regarding claim 4, Davis discloses wherein said exclusion setting section selects the particular input channel (the channel of the I/O module) to be excluded from among the plurality of input channels and lowers the signal of the selected particular input channel to a small level so as to allow the signal of the selected particular input channel to be mixed into said second output channel at a small level (at node 288).

Regarding claim 5, Davis discloses further comprising a mixing processing device (figure 2 item 12) that performs mixing processing on audio signals inputted via the input channels and outputs the audio signals, having been subjected to the mixing processing, to the output channels.

Regarding claim 6, Davis discloses wherein said first signal path setting section includes a plurality of switches (figure 2 items 20, and 22), provided in corresponding relation to the plurality of input channels, for making settings such that the signals of the input channels are mixedly outputted to a predetermined first output channel, and, in accordance with the settings made by said switches, the signals of the input channels corresponding to said switches are set to be mixed into said predetermined first output channel, and

where said second signal path setting section includes a branch (switch 200, node 288, 232 and surrounding circuitry) for branching the signals of the input channels, having been set via said switches to be mixed into said predetermined first output channel, and the signals of the input channels, having the signal of the particular input channel excluded therefrom by means of said branch, are mixed into a predetermined second output channel of the plurality of output channels.

Regarding claim 7, Davis discloses wherein said first output channel comprises a predetermined pair of mixing buses (144, 288),

wherein said first signal path setting section includes a plurality of switches (figure 2 items 20, and 22), provided in corresponding relation to the plurality of input channels, for making settings such that the signals of the input channels are outputted to said predetermined pair of mixing buses, and, in accordance with the settings made by said switches, the signals of the input channels corresponding to said switches are set to be mixed into said predetermined pair of mixing buses, and

where said second signal path setting section includes a branch (switch 200, node 288, 232 and surrounding circuitry) for branching the signals of the input channels, having been set via said switches to be mixed into said predetermined pair of mixing buses, to one of the mixing buses, and the signals of the input channels, having the signal of the particular input channel excluded therefrom by means of said branch, are mixed into the one mixing bus (288).

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Regarding claim 8, Davis discloses wherein said exclusion setting section mixes respective signals of the plurality of input channels into said second output channel via level adjusting devices (206, faders, and associated op-amps), and a signal level of a desired one of the input channels is suppressed via the level adjusting device of the desired input channel so that the desired input channel is set as the particular input channel to be excluded.

Regarding claim 9, Davis discloses wherein said second signal path setting section adjusts the signals of the input channels, other than the particular input channel to be excluded, to predetermined levels (given by position of faders) and then mixes the adjusted signals into said second output channel.

Regarding claim 10, Davis discloses a mixing signal-path setting apparatus for making a setting such that signals of one or more input channels selected from among a plurality of input channels are mixed into at least one of a plurality of output channels, said mixing signal path setting apparatus comprising:

a first signal path setting section (Figure 3, mix/assign pins 138) that selects one or more input channels from among the plurality of input channels and makes a setting such that signals of the selected one or more input channels are mixed into a first output channel (figure 2, to item 26) of a plurality of output channels;

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an exclusion setting section (direct/mix-minus select pins 110, and associated circuitry) that sets a particular input channel, from the plurality of input channels, to be excluded;

a second signal path setting section (switch 200, node 288, 232 and surrounding circuitry) that makes a setting such that the signals of the one or more input channels selected via said first signal path setting section, having the signal of the particular input channel set by said exclusion setting section excluded therefrom, are mixed into a second output channel (to item 34) of the plurality of output channels; and

a section that provides a visual display (figure 2, user interface of 10) to be used for setting various parameters for mixing signal paths.

Regarding claim 11, Davis discloses where, when the various parameters are to be set via the display, said second signal path setting section automatically performs a switching operation necessary for mixing the signals of the input channels (via console 10), having the signal of the particular input channel excluded therefrom, into said second output channel, and said second signal path setting section also automatically performs necessary signal level adjustment (via faders) in connection with the switching operation.

Regarding claim 12, Davis discloses an apparatus which further comprises a storage section (figure 2, memory on bottom) that stores information indicative of at least a portion of the settings made by said first signal path setting section, said

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exclusion setting section and said second signal path setting section, and wherein the information stored in said storage section is read out to reproduce at least a portion of the settings made by said first signal path setting section, said exclusion setting section and said second signal path setting section (column 3 lines 60-67).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Davis (US 5454041).

Regarding claim 13, claim 13 is rejected in an analogous manner to claim 1 as above. Although Davis does not expressly disclose the use of a computer program, it was well known that general-purpose computers could be used to perform signal processing or circuit simulation. The motivation for using such would have been cheaper, faster implementation and reuse of resources. Therefore it would have been obvious to one of ordinary skill in the art to perform the operations on a computer via a program stored on a computer readable medium.

Response to Arguments

Applicant's arguments filed 08/22/07 have been fully considered but they are not persuasive.

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., channels excluded at a stage preceding the mixing output section, not having been mixed) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

The examiner would like to point out that no order of operations is recited in the claim language, and that regardless of the order of mixing, the resultant end result mix of Davis is as claimed.

Regarding claim 10 the examiner maintains that user interface 10 is a visual display. The mixer console contains controls whereby visual information such as positions of knobs and faders is displayed.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

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§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Douglas Suthers whose telephone number is (571)272-0563. The examiner can normally be reached on 8am - 5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Vivian Chin can be reached on (571)272-7848. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should

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you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

djs

DD

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